Benny Hermon Govindakuttan

[bennyhermon555@gmail.com](mailto:bennyhermon555@gmail.com)

9494671968- chirala,Bapatla.

Core competancy

* Acquired in depth knowledge and proficient in ASIC PD Flow involving Floor planning, Power planning, IR Drop Analysis, Automatic P&R, CTS and Routing.
* Worked on floor plans for high utilization ratio and good contiguous core area. Worked on placement plan with power aware and acceptable congestion ensuring good routability.
* Analyzed and understood design constraints to specify PVT corners, False paths, Half Cycle, Multi Cycle paths and fixed the setup and hold violations.
* Good Knowledge in Logic Design Concepts, CMOS Semiconductor Theory and Basic Electronic Devices. Hands on Experience in APR Tools - Synopsys ICC2
* I have knowledge in the Synthesis Flow i.e. RTL to Gate Level Net list.

Educational details

Bachelor Degree in Electrical and Electronics and Engineering 2022  
QIS , with 7.13 CGPA 2022.

Diploma 2019

St Ann’s college of engineering and technology with 71.48 % .

SSC 2016

Sri Gowthami high school , with 8.2 CGPA.

Domain Specific Project

Graduate Trainee Engineer at VLSI GURU institute July-2022 to Dec-2022

RCR TOP Description

Technology-28nm, Metal Layers-9, Clock Frequency-833MHz, Supply Voltage-1.1V, Vt of transistors-lvt, svt, hvt, Area-4.2 Sq mm, Macro Count-42, Standard Cell Count-200K.

Tools

Synopsys IC Compiler2

Challenges

Understanding the design constraints. By using Fly lines designed the Floorplan to have uniform core area for standard cells.

Building a good power plan to meet the IR drop specified and ensuring that no floating pins, missing vias in the design and no PG DRC errors after building the power network.

To control congestion and DFT aware placement, tried different floorplan experiments and implemented different strategies.

Understanding tool's behavior while CTS to meet target skew, fixing timing violations, understanding DRC and LVS errors.ional det

B.E / B.Tech Academic Project

QISCET

Industrail Stepper motor directional control using DTMF

In this project direction of stepper motor is controlled using DTMF module. It is helpful in the present current world since stepper motors are utilized in different fields like mechanical apparatus, mechanical technology, computer peripherals, business machines, movements control, clinical, different cycle control and machine-device applications.

Tools

LCD display, Buzzer, Aurdino UNO ,ULN2003,DTMF module ,stepper motor.ails